

In the Claims

Please amend the claims as follows:

1. (Amended) A monolithic video signal processing circuit comprising within a single substrate:

means for accepting a video signal;

A₁ 5 means for detecting the amplitude of accepted signals and for amplifying said accepted signals to a specific level; and

means for accepting said specific level amplified video signals and for processing said amplified signals to reduce all but the intermediate frequencies (IF) [frequencies] present in said video signals while amplifying said IF frequencies to a certain fixed value for presentation to an output of said circuit.

A₂ 4. (Amended) The invention set forth in claim 1 wherein said detecting and amplification means is a variable gain amplifier (VGA).

7. (Amended) The method of processing a video signal comprising the steps of:

presenting said video signal to the input of a monolithic circuit;

A₃ 5 detecting the amplitude of presented signals and amplifying said presented signals to a specific level;

accepting said specific level amplified video signals and further processing said amplified signals to reduce all but the intermediate frequencies (IF) [frequencies] present in said video signals while amplifying said IF frequencies to a certain fixed value for presentation to an output of said monolithic circuit.

A₄ 10. (Amended) The method set forth in claim 7 wherein the amplification of the amplification and detection step is accomplished by a variable gain amplifier (VGA).

13. (Amended) A circuit [system] for processing radio frequency (RF) signals [in a monolithic circuit,] comprising:

an input to said circuit for receiving an [on] RF signal;

5 a mixer having [one] an input [and one output, said input of said mixer means] connected to said RF signal input;

A₅ a first filter having [one] an input [and one output, said input of said first filter] connected to [said] an output of said mixer;

a first amplifier having [one] an input [and one output, said input of said amplifier] connected to [said] an output of said first filter;

10 a second filter having [one] an input [and one output, said input of said second filter] connected to [said] an output of said first amplifier; and

a second amplifier having [one] an input [and one output, said input of said second amplifier] connected to [said] an output of said second filter, and [said] an output connected to an output of said circuit;

15 wherein said mixer, said first and second filters and said first and second amplifiers are constructed on a single integrated substrate.

A₆ 15. (Amended) The system as claimed in claim 13, wherein said first amplifier means is a variable gain amplifier (VGA).

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cont. 17. (Amended) The system as claimed in claim 13, wherein said second amplifier means is [an] a fixed gain amplifier (FGA).